



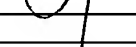


LIST OF DOCUMENTS CITED BY APPLICANTS (Use several sheets if necessary)	ATTY. DOCKET NO. 7081M	SERIAL NO. 09/275,495
	APPLICANT Vladimir Gartstein, et al.	
	FILING DATE 3/24/99	GROUP 1745

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	4,084,124	4/11/78	Kapustka	320	9	
	4,105,962	8/8/78	Scott Jr. et al.	320	32	
	4,121,115	10/17/78	de Méré	307	150	
	4,209,736	6/24/80	Reidenbach	320	22	
	4,289,836	9/15/81	Lemelson	429	61	
	4,296,461	10/20/81	Mallory et al.	363	22	
	4,433,278	2/21/84	Lowndes et al.	320	48	
	4,451,743	5/29/84	Suzuki et al.	307	110	
	4,542,330	9/17/85	Terbrack	323	222	
	4,563,627	1/7/86	Orban	320	19	
	4,611,161	9/9/86	Barker	320	2	
	4,633,379	12/30/86	Oda et al.	363	19	
	4,686,444	8/11/87	Park	320	31	
	4,716,354	12/29/87	Hacker	320	39	
	4,727,006	2/23/88	Malinowski et al.	429	50	
	4,737,702	4/12/88	Koenck	320	40	
	4,771,226	9/13/88	Jones	323	303	
	4,816,737	3/28/89	Delmas et al.	320	35	
	4,818,928	4/4/89	Schosser	320	2	
	4,998,056	3/5/91	Cole	320	35	
	5,006,881	4/9/91	Kodama	354	484	
	5,032,825	7/16/91	Kuznicki	340	636	
	5,045,768	9/3/91	Pelly	320	2	
	5,161,097	11/3/92	Ikeda	363	124	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
		WO 93/23887	11/25/93	PCT	H01M	8/10		
		WO 94/00888	1/6/94	PCT	H01M	10/48		
		WO 97/13189	4/10/97	PCT	G06F	1/26		
		WO 97/18588	5/22/97	PCT	H01L	27/02		
		0 546 872	6/16/93	Europe	H02J	7/10		
		0 624 944	7/29/98	Europe	H02J	7/36		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Allen, Charlie, CMOS curbs the appetite of power-hunger dc-dc converter chips, Electronic Design, 11/14/85, pp. 175-79.
	Allen, Charlie, DC/DC chip for low power circuit efficiency, Electronic Product Design, 8/96, pp. 29-32.
	Arbetter, Barry and Maksimovic, Dragan, Control Method for Low-Voltage DC Power Supply in Battery-Powered Systems with Power Management, IEEE 1997, pp. 1198-1204.
	EXAMINER
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	



LIST OF DOCUMENTS CITED BY APPLICANTS (Use several sheets if necessary)	ATTY. DOCKET NO. 7081M	SERIAL NO. 09/275,495
	APPLICANT Vladimir Gartstein, et al.	
	FILING DATE 3/24/99	GROUP 1745

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	5,168,206	12/1/92	Jones	320	31	
	5,204,608	4/20/93	Koenck	320	2	
	5,206,097	4/27/93	Burns et al.	429	90	
	5,216,371	6/1/93	Nagai	324	428	
	5,218,284	6/8/93	Burns et al.	320	14	
	5,245,269	9/14/93	Tooley et al.	320	35	
	5,247,238	9/21/93	Yang	320	35	
	5,248,929	9/28/93	Burke	320	48	
	5,258,239	11/2/93	Kobayashi	429	27	
	5,264,301	11/23/93	Sindorf et al.	429	53	
	5,280,420	1/18/94	Rapp	363	60	
	5,296,765	3/22/94	Williams et al.	307	572	
	5,304,431	4/19/94	Schumm, Jr.	429	27	
	5,339,236	8/16/94	Tamagawa	363	59	
	5,343,088	8/30/94	Jeon	307	296.2	
	5,355,073	10/11/94	Nguyen	320	15	
	5,394,365	2/28/95	Tsukikawa	365	189.09	
	5,446,367	8/29/95	Pinney	323	266	
	5,449,569	9/12/95	Schumm, Jr.	429	27	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION	
						YES	NO
	0 653 798	5/17/95	Europe	H01M	10/46		
	0 653 841	5/17/95	Europe	H03K	17/16		
	10-262365	9/29/98	Japan	H02M	3/155		X
	10-284099	10/23/98	Japan	H01M	8/04		X
	10-285809	10/23/98	Japan	H02J	7/00		X
	10-285820	10/23/98	Japan	H02J	7/10		X
	61-294,754	12-25-86	Japan	H01M	2/02		
	726608	5/4/80	Russian	H01M	10/48		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

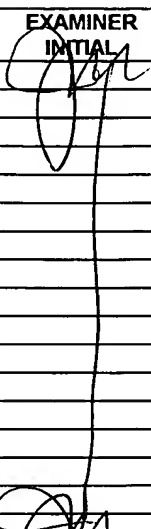
	Goodenough, Frank, Off-Line and One-Cell IC Converters Up Efficiency, Electronic Design, 6/27/94, pp. 55-64.
	Iwata, T. Yamauchi, H. Akamatsu, Y. Terada, A. Matsuzawa, Gate-Over Driving CMOS Architecture for 0.5V Single-Power-Supply-Operated Devices, IEEE 1997, pp. 290-91 and 473.
	McClure, M., Constant Input Power Modulation Technique for High Efficiency Boost Converter Optimized for Lithium-Ion Battery Applications, IEEE 1996, pp. 850-55.
	EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

DATE CONSIDERED 9/29/01



LIST OF DOCUMENTS CITED BY APPLICANTS (Use several sheets if necessary)	ATTY. DOCKET NO. 7081M	SERIAL NO. 09/275,495
	APPLICANT Vladimir Gartstein, et al.	
	FILING DATE 3/24/99	GROUP 1745

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	5,499,183	3/12/96	Kobatake	363	59	
	5,541,016	7/30/96	Schumm, Jr.	429	27	
	5,541,489	7/30/96	Dunstan	320	2	
	5,541,490	7/30/96	Sengupta et al.	320	14	
	5,557,188	9/17/96	Piercey	320	5	
	5,560,999	10/1/96	Pedicini et al.	429	27	
	5,561,361	10/1/96	Sengupta et al.	320	14	
	5,563,004	10/8/96	Buzzelli et al.	429	27	
	5,565,759	10/15/96	Dunstan	320	48	
	5,583,415	12/10/96	Fernandez et al.	320	15	
	5,590,419	12/31/96	Shimo	455	127	
	5,591,212	1/7/97	Keimel	607	5	
	5,592,069	1/7/97	Dias et al.	320	30	
	5,600,230	2/4/97	Dunstan	320	48	
	5,606,242	2/25/97	Hull et al.	320	48	
	5,610,450	3/11/97	Saeki et al.	307	46	
	5,619,430	4/8/97	Nolan et al.	364	557	

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES NO
GB 2 270 793	3/23/94	United Kingdom	H01M	10/44	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Mohandes, B., MOSFET Synchronous Rectifiers Achieve 90% Efficiency - Part II, PCIM, July 1991 pp. 55-61.
	Moore, B., Regulator topologies standardize battery-powered systems, EDN, 1/20/94, pp. 59-64.
	Neil, C., Smart Battery Power, Australian Electronics Engineering, April 1996, pp. 34-38.
	Travis, B., Low-voltage power sources keep pace with plummeting logic and μ P voltages, EDN, 9/26/96, pp. 51-62.
	Williams, R., Mohandes, B. and Lee, C., High-Frequency DC/DC Converter for Lithium-Ion battery Applications Utilizes Ultra-Fast CBiC/D Process Technology, IEEE 1995, pp. 322-332.
EXAMINER	DATE CONSIDERED 9/29/91
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Sheet 4 of 6

FORM PTO-1449

LIST OF DOCUMENTS CITED BY APPLICANTS (Us several she ts if necessary)	ATTY. DOCKET NO. 7081M	SERIAL NO. 09/275,495
	APPLICANT Vladimir Gartstein, et al.	
	FILING DATE 3/24/99	GROUP 1745

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	5,633,573	5/27/97	van Phuoc et al.	320	5	
	5,645,949	7/8/97	Young	429	7	
	5,646,508	7/8/97	van Phuoc et al.	320	30	
	5,656,876	8/12/97	Radley et al.	307	150	
	5,675,232	10/7/97	Koenck	320	2	
	5,689,178	11/18/97	Otake	323	282	
	5,694,024	12/2/97	Dias et al.	320	30	
	5,710,501	1/20/98	Van Phuoc et al.	320	2	
	5,714,863	2/3/98	Hwang et al.	320	1	
	5,731,686	3/24/98	Malhi	320	35	
	5,747,189	5/5/98	Perkins	429	91	
	5,783,322	7/21/98	Nagai et al.	429	7	
	5,825,156	10/20/98	Patillon et al.	320	21	
	5,831,418	11/3/98	Kitagawa	323	222	
	5,837,394	11/17/98	Schumm, Jr.	429	27	

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Yamauchi, H, Iwata, T., Akamatus, H. and Matsuzawa, A., A 0.5V/100MHz Over-Vcc Grounded Data Storage (OVGS) SRAM Cell Architecture With Boosted Bit-line and offset Source Over-Driving Schemes, 1996 International Symposium on Low Power Electronics and Design, 8/12-14/96, pp. 49-54.
	Zhou, X., Wang, T. and Lee, F., Optimizing Design for Low voltage DC-DC Converters, IEEE 1997, pp. 612-616.
	Aguilar, C., Canales, F., Arau, J., Sebastian, J. and Uceda, J., An Integrated Battery Charger/ Discharger with Power-Factor Correction, IEEE Transactions on Industrial Electronics, Oct. 1997, pp. 597-603.
	Favrat, P., Paratte, L., Ballan, H. Declercq, M. and deRoij, N., A 1.5-V-Supplied CMOS ASIC for th Actuation of an Electrostatic Micromotor, IEEE/ASME Transactions on Mechatronics , Sept. 1997, pp. 153-160.
	Wolfgang, G. and Lindemann, R., New Topology for High Efficient DC/DC Conyersion, Power Conversion, May 1996, pp. 503-512.
EXAMINER	DATE CONSIDERED
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Patent and Trademark Office - U.S. DEPARTMENT OF COMMERCE

Sheet 5 of 6

FORM PTO-1449

LIST OF DOCUMENTS CITED BY APPLICANTS (Use several sheets if necessary)	ATTY. DOCKET NO. 7081M	SERIAL NO. 09/275,495
	APPLICANT Vladimir Gartstein, et al.	
	FILING DATE 3/24/99	GROUP 1745

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Lachs, W., Sutanto, D. and Logothetis, D., Power System Control in the Next Century, IEEE Transactions on Power Systems, Feb. 1996, pp. 11-18.	
	Wells, Eddy, Integrated Boost Controller Extends alkaline usage, Electronic Design, 11/17/97, v. 45,n. 25, p. S54(1).	
	Stratakos, Anthony, High-Efficiency Low-Voltage DC-DC Conversion for Portable Applications, University of California, Berkeley, Department of EECS, http://bob.eecs.berkeley.edu	
	Alfano, Don, Dc/Dc converters for battery-operated systems: Charge pump or switcher?, Electronic Products, August 1996	
	Douseki, Takakuni, et al., A 0.5-V MYCMOS/SIMOX Logic Gate, IEEE Journal of Solid-State Circuits, v. 32, no. 10, pp.1604-09, October 1997.	
	Wang, Chi-Chang, et al., Efficiency Improvement in Charge Pump Circuits, IEEE Journal of Solid State Circuits, vol. 32, No. 6, June 1997, pp. 852-60.	
	Sherman, Len, DC/DC converters adapt to the needs of low-power circuits, EDN, January 7, 1998, pp.145-52.	
	Heacock, David, and Freeman, David, Capacity Monitoring in Advanced Battery Chemistries, IEEE, 1995 (0-7803-2459-5/95).	
	Matsuya, Yasuyuki, et al., Low-Voltage Supply A/D, D/A Conversion Technology, Institute of Electronics, Information and Communications, Engineers Technical Report of IEICE, August 1994.	
	Yamasaki, Mikio, et al., High-efficiency power source with low input voltage, National Meeting Fall 1989, Institute of Electronics and Communication Engineers of Japan, pp. 3-276; 277.	
	Kida, Junzo, et al., Performance of DC-DC converter under battery load, Doshisha University, PE 90 37, Serial No. 0005, pp. 29-35.	
	Tsukamota, Kazuo, et al., Low-power dissipation technology of control circuit for battery-input-type power source, Technical Report of IEICE, PE 94-71 (1995-03), The Institute of Electronics, Information and Communication Engineers.	
	EXAMINER	DATE CONSIDERED 9/29/01
	EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Patent and Trademark Office - U.S. DEPARTMENT OF COMMERCE

